



DIM1200ASM45-TL001

Single Switch IGBT Module

Replaces DS6169-1

DS6169-2 August 2015 (LN32854)

FEATURES

- 10.2kV Isolation
- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density Enhanced DMOS SPT
- Isolated AISiC Base With AIN Substrates

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM1200ASM45-TL001 is a single switch 4500V, n-channel enhancement mode, insulated gate bipolar transistor (IGBT) module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM1200ASM45-TL001

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		4500V
V _{CE(sat)}	* (typ)	2.3V
l _c ` ´	(max)	1200A
I _{C(PK)}	(max)	2400A

* Measured at the auxiliary terminals

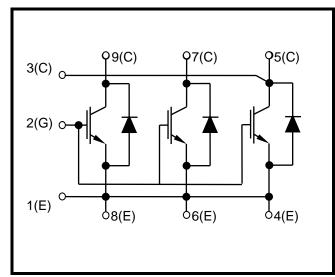


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	4500	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 95°C	1200	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	2400	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 125^{\circ}C$	12.5	kW
l ² t	Diode I ² t value	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C	460	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	10.2	KV
Q_{PD}	Partial discharge – per module	IEC1287, $V_1 = 6900V$, $V_2 = 5100V$, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

56mm

26mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor	Continuous dissipation - junction to case	-	-	8	°C/kW
R _{th(j-c)}	Thermal resistance – diode	Continuous dissipation - junction to case	-	ı	16	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	1	6	°C/kW
T _j	Junction temperature	Transistor	-	-	125	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	ı	125	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
1	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			4	mA
I _{CES}		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			90	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
V _{GE(TH)}	Gate threshold voltage	$I_C = 120$ mA, $V_{GE} = V_{CE}$		5.8		V
	Collector-emitter saturation voltage	V _{GE} = 15V, I _C = 1200A		2.3		V
V _{CE(sat)}		V _{GE} = 15V, I _C = 1200A, T _j = 125°C		2.9		V
I _F	Diode forward current	DC		1200		Α
I _{FM}	Diode maximum forward current	t _p = 1ms		2400		Α
	Diode forward voltage	I _F = 1200A		2.8		V
V_{F}		I _F = 1200A, T _j = 125°C		3.2		V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		150		nF
Qg	Gate charge	±15V Including external C _{ge}		17		μC
C _{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		12		nF
L _M	Module inductance			10		nΗ
R _{INT}	Internal transistor resistance			90		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}\text{C}, V_{CC} = 3400\text{V}$ $t_{p} \le 10\mu\text{s}, V_{GE} \le 15\text{V}$ $V_{CE (max)} = V_{CES} - L^{*} x dI/dt$ IEC 60747-9		4800		A

Note:

L is the circuit inductance + L_{M}



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time			3000		ns
t _f	Fall time	$I_{C} = 1200A$ $V_{GF} = \pm 15V$		600		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 2800V		5100		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.4\Omega$ $R_{G(OFF)} = 2.7\Omega$ $C_{ge} = 220 nF$ $L_{S} \sim 165 nH$		900		ns
t _r	Rise time			350		ns
E _{ON}	Turn-on energy loss			4800		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 1200A		1340		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 2800V$		1030		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 3000A/\mu s$		2220		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time			3100		ns
t _f	Fall time	$I_{C} = 1200A$ $V_{GE} = \pm 15V$		560		ns
E _{OFF}	Turn-off energy loss	V _{CE} = 2800V		5200		mJ
t _{d(on)}	Turn-on delay time	$R_{G(ON)} = 2.4\Omega$ $R_{G(OFF)} = 2.7\Omega$ $C_{ge} = 220 nF$ $L_{S} \sim 165 nH$		900		ns
t _r	Rise time			360		ns
E _{ON}	Turn-on energy loss			6450		mJ
Q_{rr}	Diode reverse recovery charge	I _F = 1200A		2200		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 2800V$		1100		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 3000A/\mu s$		3750		mJ



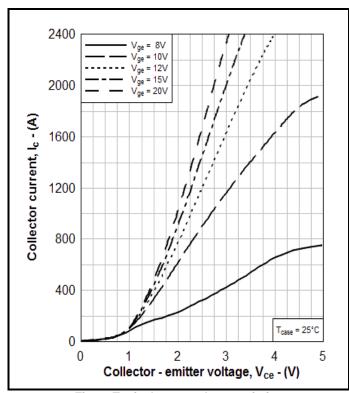


Fig. 3 Typical output characteristics

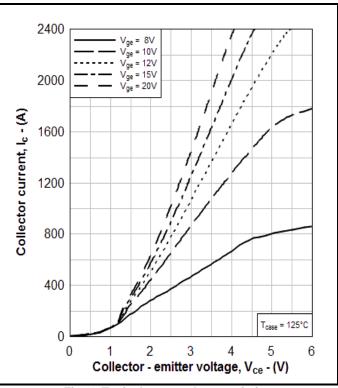


Fig. 4 Typical output characteristics

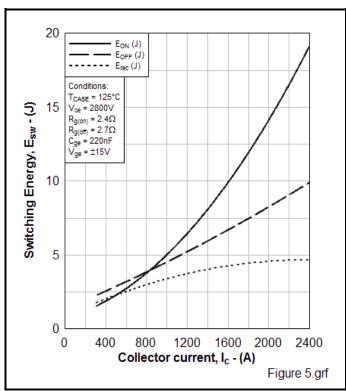


Fig. 5 Typical switching energy vs collector current

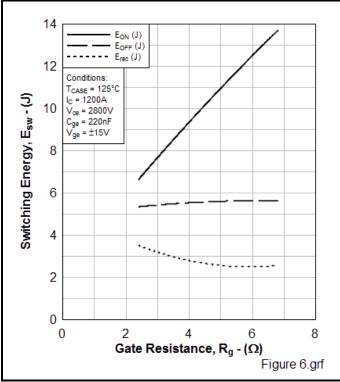


Fig. 6 Typical switching energy vs gate resistance



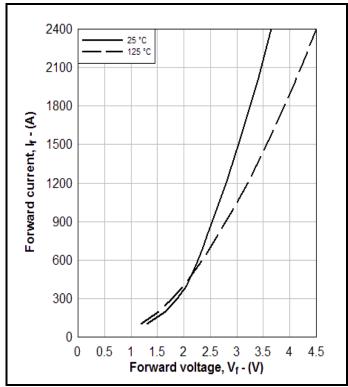


Fig. 7 Diode typical forward characteristics

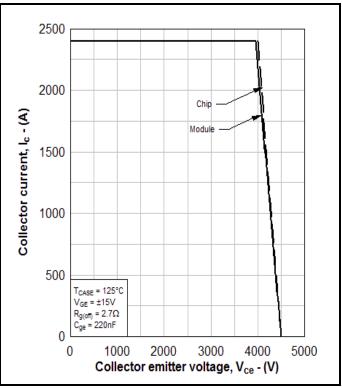


Fig. 8 Reverse bias safe operating area

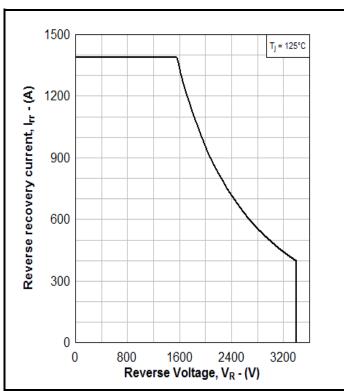


Fig. 9 Diode reverse bias safe operating area

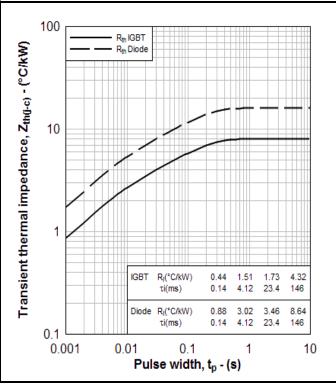


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

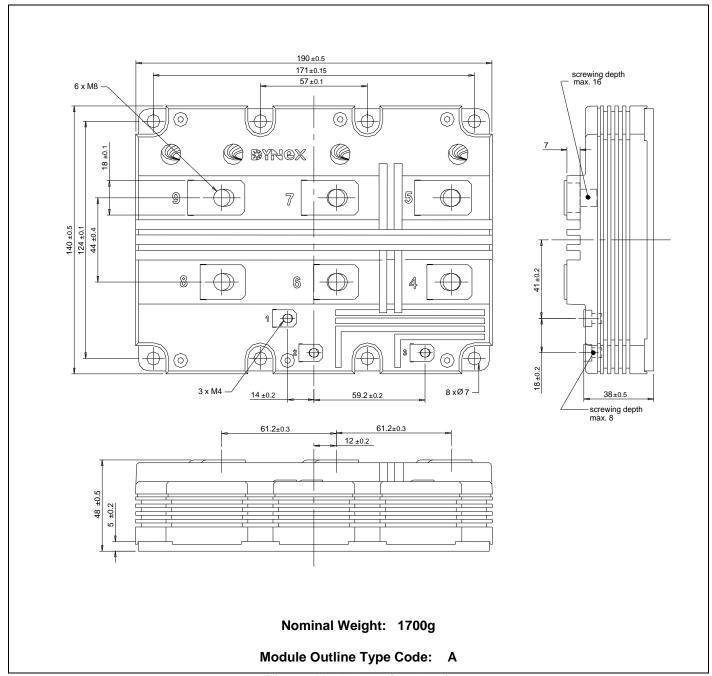


Fig. 11 Module outline drawing



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