

DIM400XCM33-F000

IGBT Chopper Module

Replaces DS5938-1.0

DS5938-2 May 2011 (LN28404)

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- Soft Punch Through Silicon
- Isolated AlSiC Base with AlN Substrates
- Lead Free Construction
- 10.2kV Isolation Package

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Traction Drives
- Choppers

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM400XCM33-F000 is a 3300V, soft punch through n-channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM400XCM33-F000

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		3300V
V _{CE(sat)}	* (typ)	2.8V
l _c ` ´	(max)	400A
I _{C(PK)}	(max)	800A

^{*} Measured at the auxiliary terminals

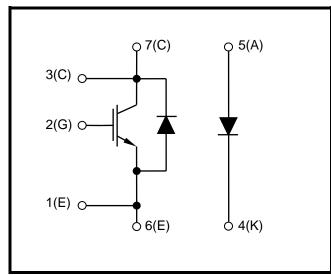


Fig. 1 Circuit configuration



Fig. 2 Package



ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	3300	V
V_{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _{case} = 90°C	400	Α
I _{C(PK)}	Peak collector current	1ms, T _{case} = 115°C	800	Α
P _{max}	Max. transistor power dissipation	$T_{case} = 25^{\circ}C, T_{j} = 150^{\circ}C$	5200	W
l ² t	Diode I ² t value (IGBT arm)	V 0 + 10mg T 1250C	80	kA ² s
1 1 1	Diode I ² t value (Diode arm)	$V_R = 0$, $t_p = 10$ ms, $T_j = 125$ °C		kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz		kV
Q_{PD}	Partial discharge – per module	IEC1287, V ₁ = 6900V, V ₂ = 5100V, 50Hz RMS	10	рC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:

Baseplate material:

Creepage distance:

Clearance:

CTI (Comparative Tracking Index):

AIN

AISiC

56mm

26mm

>600

Symbol	Parameter	Test Conditions	Min	Тур.	Max	Units
R _{th(j-c)}	Thermal resistance – transistor (per arm)	Continuous dissipation – junction to case	1	-	24	°C/kW
D	Thermal resistance – diode (IGBT arm)	Continuous dissipation –	ı	-	48	°C/kW
$R_{th(j-c)}$	Thermal resistance – diode (Diode arm)	junction to case	-	-	48	°C/kW
R _{th(c-h)}	Thermal resistance – case to heatsink (per module)	Mounting torque 5Nm (with mounting grease)	-	-	8	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
		Mounting – M6	-	-	5	Nm
	Screw torque	Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm



ELECTRICAL CHARACTERISTICS

 T_{case} = 25°C unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
I _{CES}	Collector cut-off current	$V_{GE} = 0V$, $V_{CE} = V_{CES}$			2	mA
		$V_{GE} = 0V$, $V_{CE} = V_{CES}$, $T_{case} = 125$ °C			30	mA
I _{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μΑ
V _{GE(TH)}	Gate threshold voltage	$I_C = 40$ mA, $V_{GE} = V_{CE}$	5.5	6.5	7.0	V
v †	Collector-emitter saturation	V _{GE} = 15V, I _C = 400A		2.8		V
$V_{CE(sat)}^{\dagger}$	voltage	V _{GE} = 15V, I _C = 400A, T _j = 125°C		3.6		V
I _F	Diode forward current	DC		400		Α
I _{FM}	Diode maximum forward current	$t_p = 1 ms$		800		Α
	Diode forward voltage (IGBT arm)	- I _F = 400A		2.9		V
	Diode forward voltage (Diode arm)			2.9		V
V_F^{\dagger}	Diode forward voltage (IGBT arm)	1 400A T 4050Q		3.0		V
	Diode forward voltage (Diode arm)	I _F = 400A, T _j = 125°C		3.0		V
C _{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		72		nF
Q_g	Gate charge	±15V		10		μC
C_{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$		1.1		nF
L_M	Module inductance – per arm			30		nH
R _{INT}	Internal resistance – per arm			260		μΩ
SC _{Data}	Short circuit current, I _{SC}	$T_{j} = 125^{\circ}C$, $V_{CC} = 2500V$ $t_{p} \le 10\mu s$, $V_{GE} \le 15V$ $V_{CE (max)} = V_{CES} - L^{*}x dI/dt$ IEC 60747-9		1850		А

[†] Measured at the auxiliary terminals L is the circuit inductance + L_M



ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
$t_{d(off)}$	Turn-off delay time	$I_{C} = 400A$ $V_{GE} = \pm 15V$ $V_{CE} = 1800V$ $C_{ge} = 110nF$ $L_{S} \sim 100nH$	$R_{G(ON)} = 8.2\Omega$ $R_{G(OFF)} = 8.2\Omega$		2.1		μs
t _f	Fall time				210		ns
E _{OFF}	Turn-off energy loss				520		mJ
t _{d(on)}	Turn-on delay time				1130		ns
t _r	Rise time				245		ns
E _{ON}	Turn-on energy loss		$R_{G(ON)} = 5.6\Omega$ $R_{G(OFF)} = 8.2\Omega$		620		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 400A			160		μC
I _{rr}	Diode reverse recovery current	$V_{CE} = 1800V$		330		Α	
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 2$	2000A/µs		150		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Тур.	Max	Units
t _{d(off)}	Turn-off delay time	$I_{C} = 400A$ $V_{GE} = \pm 15V$ $V_{CE} = 1800V$ $C_{ge} = 110nF$ $L_{S} \sim 100nH$	$R_{G(ON)} = 8.2\Omega$ $R_{G(OFF)} = 8.2\Omega$		2.15		μs
t _f	Fall time				220		ns
E _{OFF}	Turn-off energy loss				600		mJ
t _{d(on)}	Turn-on delay time				1160		ns
t _r	Rise time				285		ns
E _{ON}	Turn-on energy loss		$R_{G(ON)} = 5.6\Omega$ $R_{G(OFF)} = 8.2\Omega$		870		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 400A V _{CE} = 1800V			300		μC
I _{rr}	Diode reverse recovery current				400		Α
E _{rec}	Diode reverse recovery energy	$dI_F/dt = 2$	2000A/µs		300		mJ



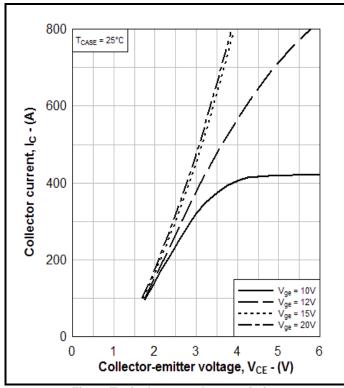


Fig. 3 Typical output characteristics

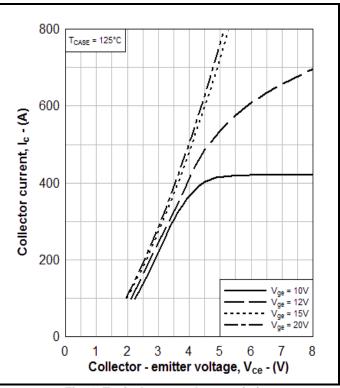


Fig. 4 Typical output characteristics

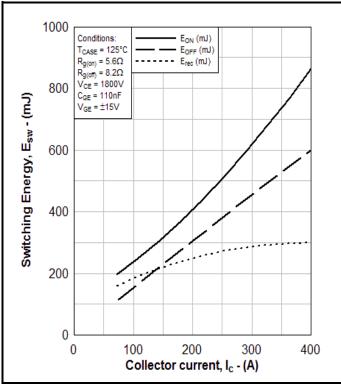


Fig. 5 Typical switching energy vs collector current

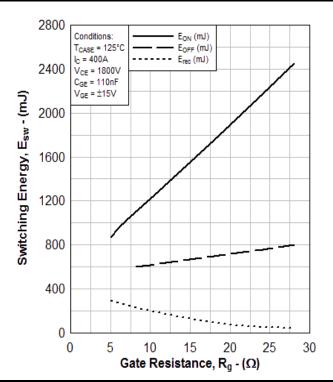


Fig. 6 Typical switching energy vs gate resistance



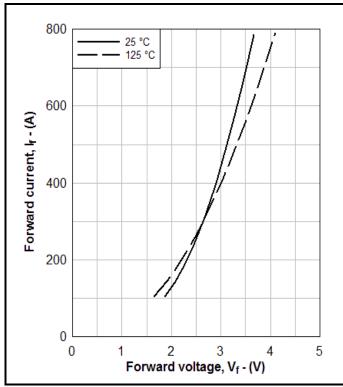


Fig. 7 Diode typical forward characteristics

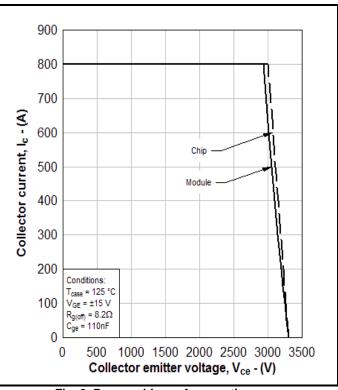


Fig. 8 Reverse bias safe operating area

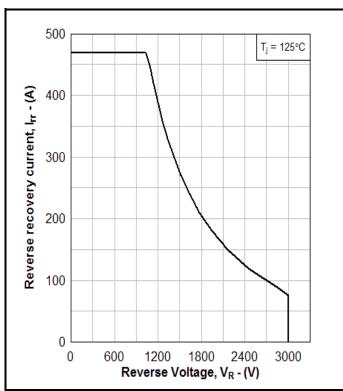


Fig. 9 Diode reverse bias safe operating area

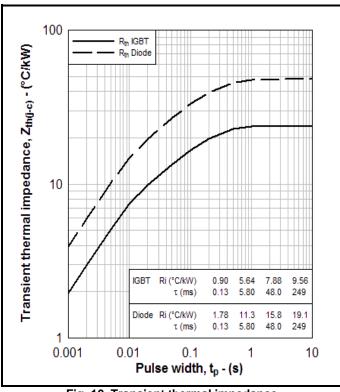


Fig. 10 Transient thermal impedance



PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.

DO NOT SCALE.

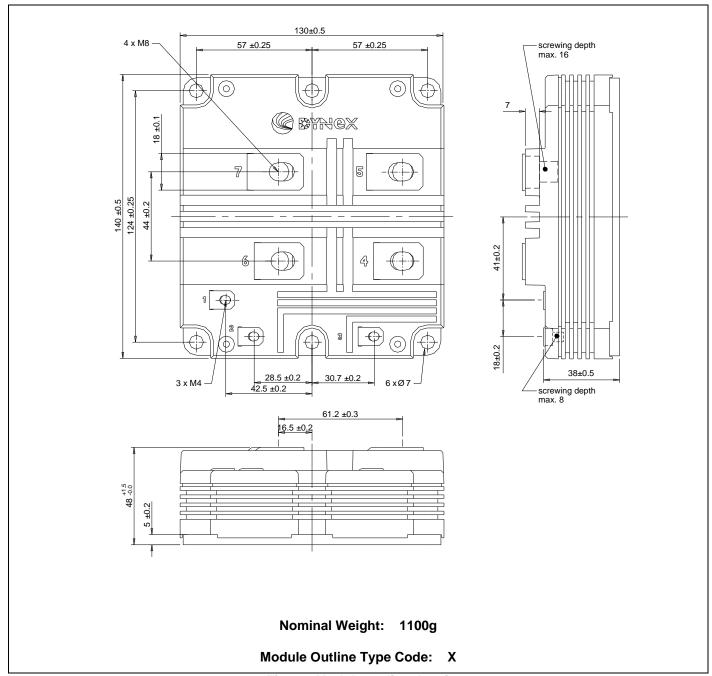


Fig. 11 Module outline drawing



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